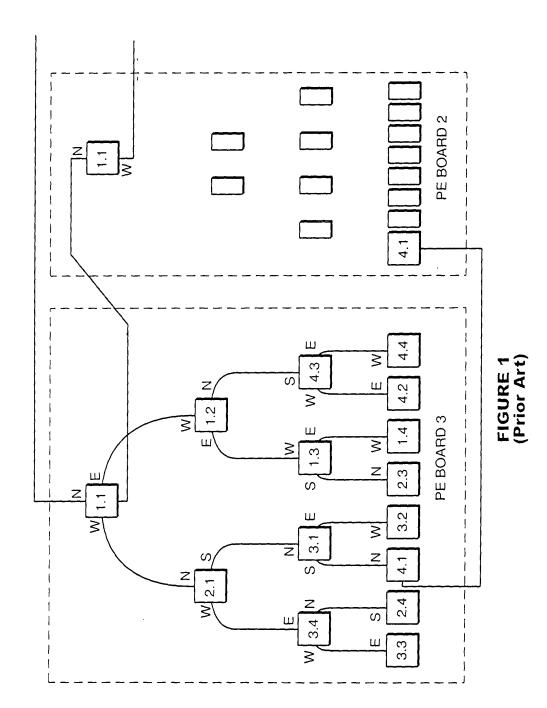
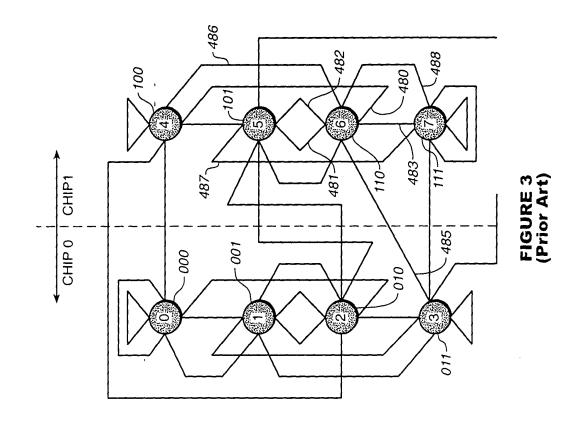
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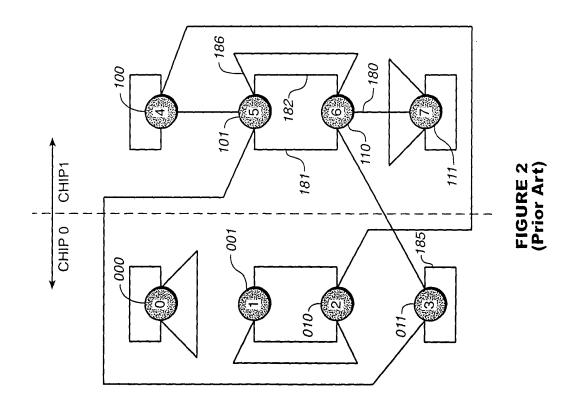


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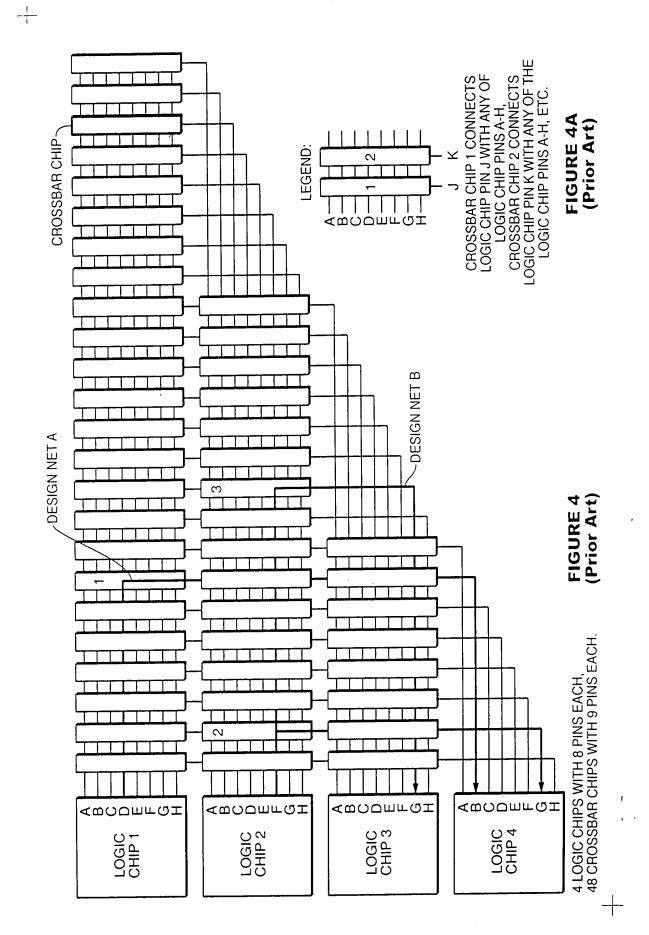
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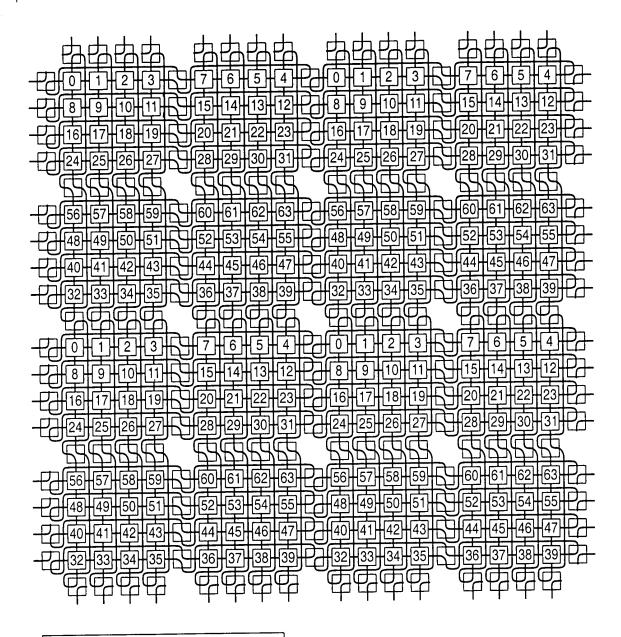


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0 ~63 :PROCESSORS (NUMBERS REPRESENT LOGICAL ID)

THROUGH SWITCH

TURN SWITCH

FIGURE 5 (Prior Art)

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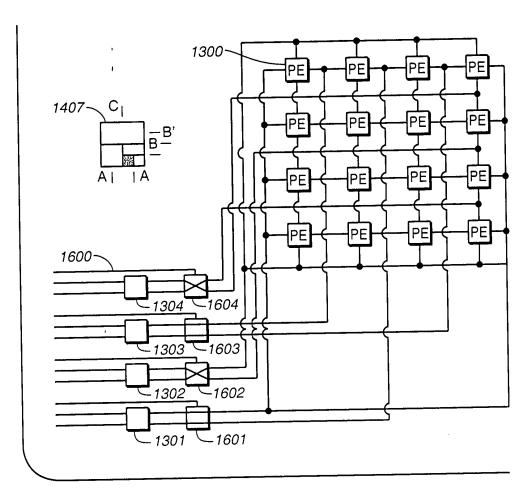


FIGURE 6 (Prior Art)

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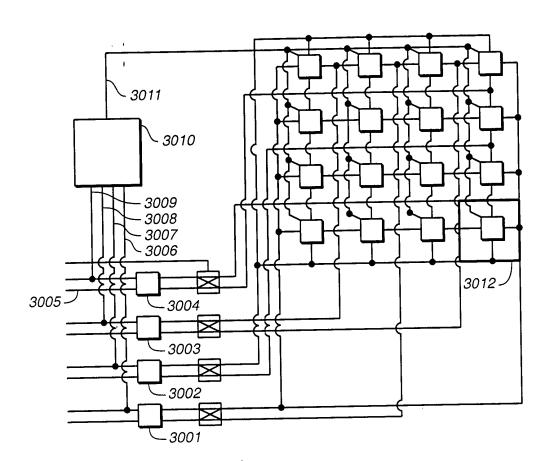
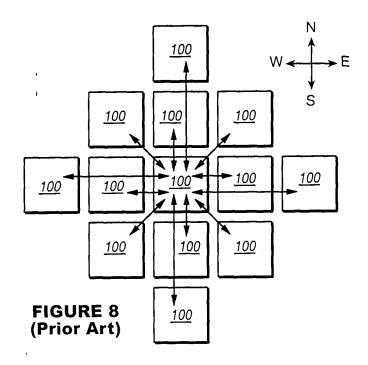
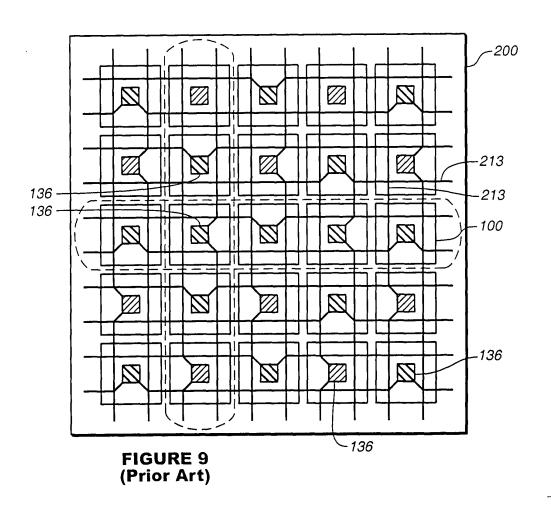


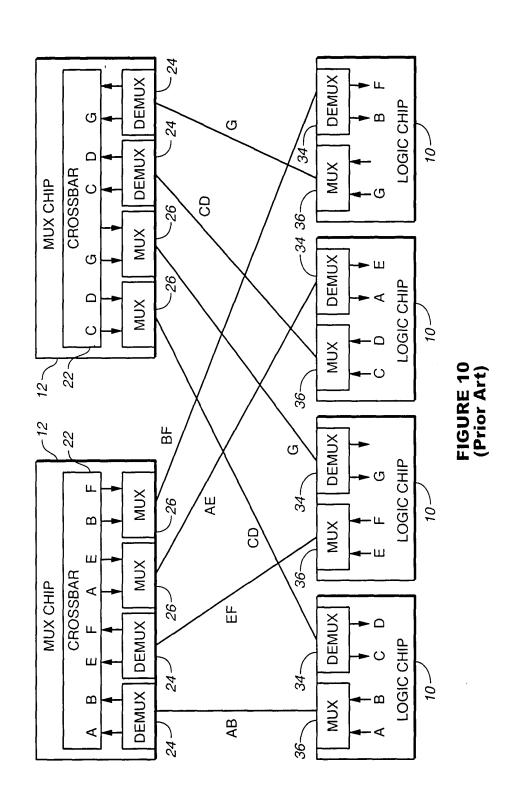
FIGURE 7 (Prior Art)

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FIGURE 11 (Prior Art)

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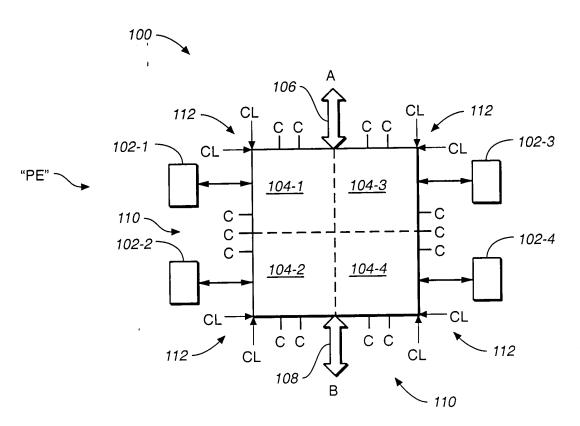


FIGURE 12

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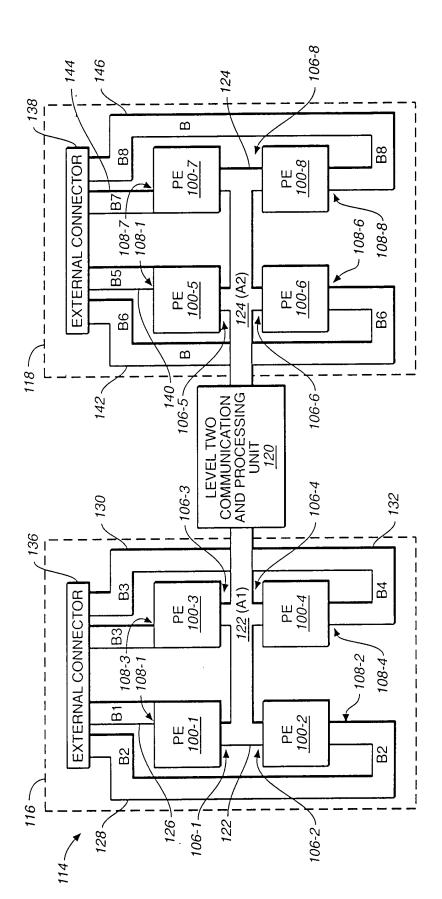
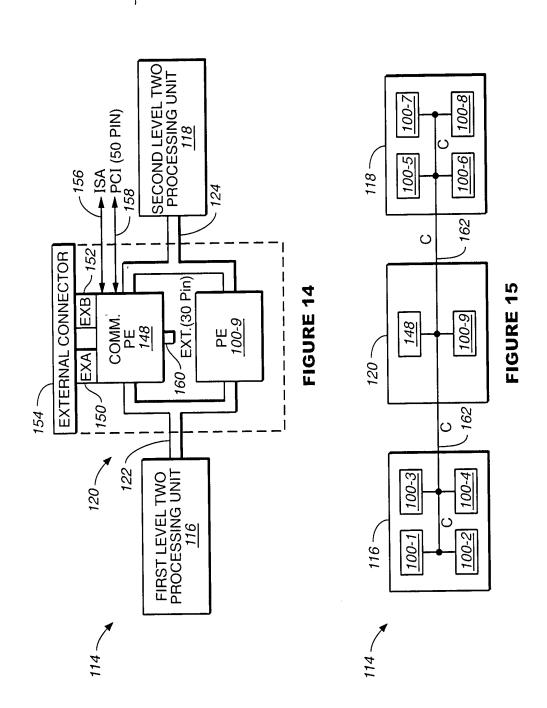


FIGURE 13

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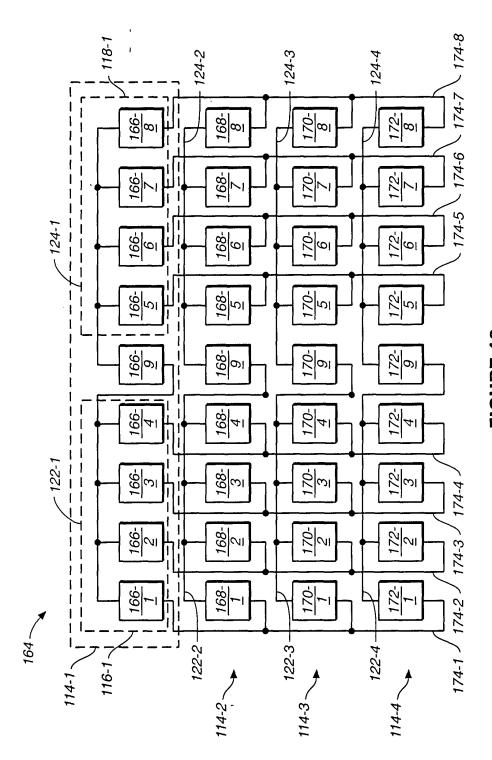


FIGURE 16

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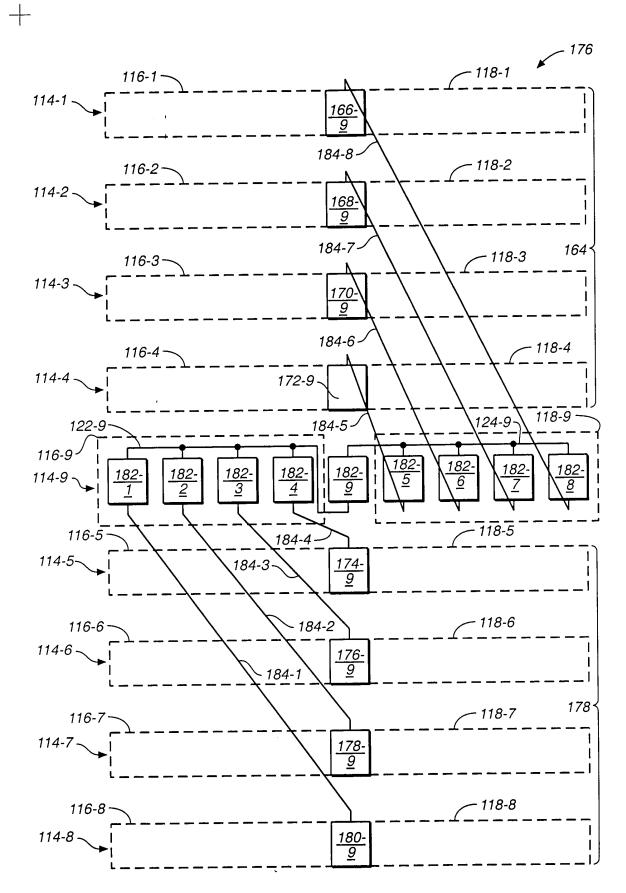


FIGURE 17

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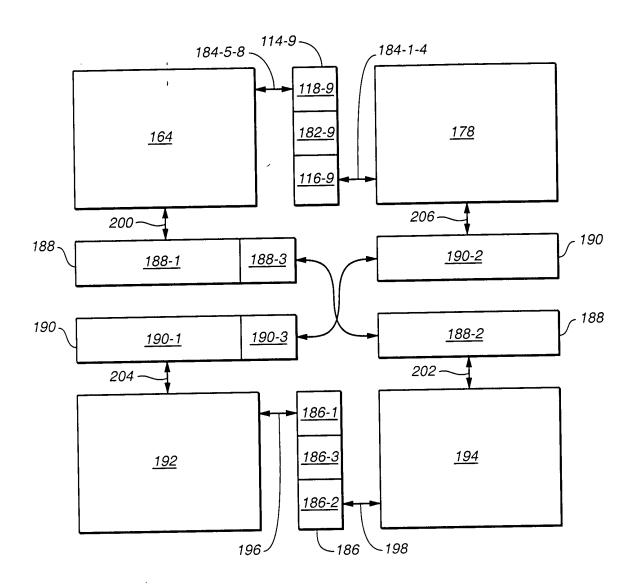
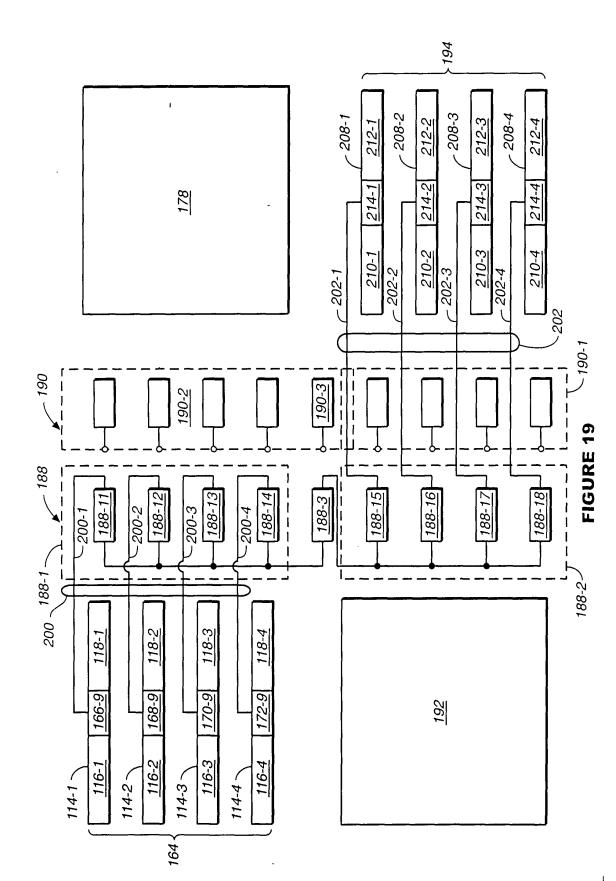
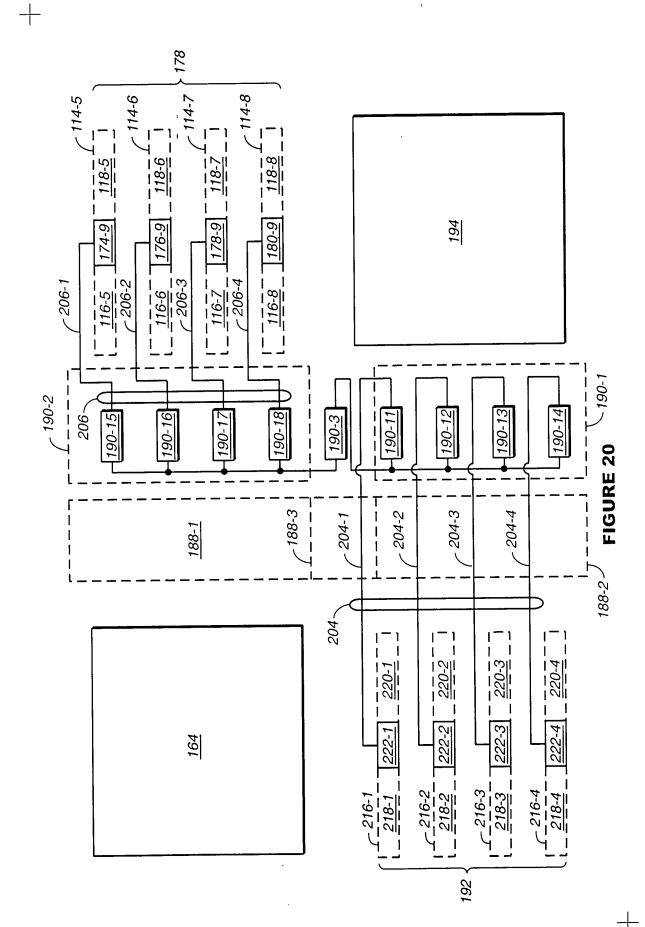


FIGURE 18

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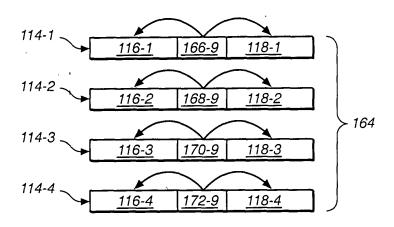
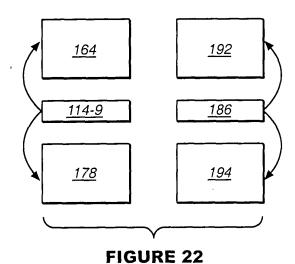


FIGURE 21



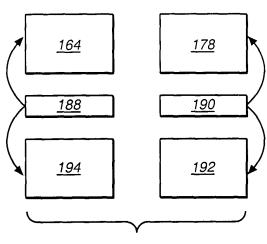
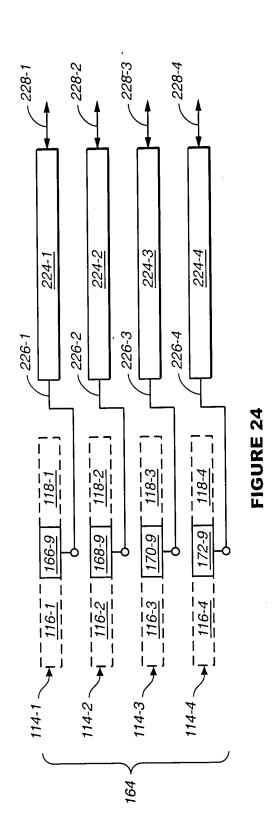
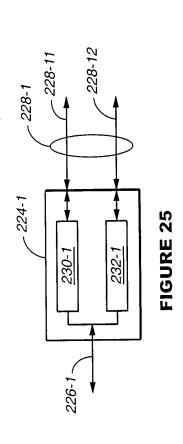


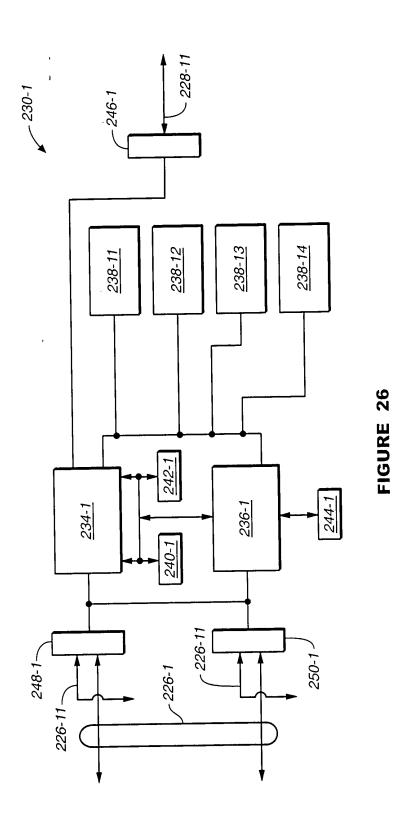
FIGURE 23

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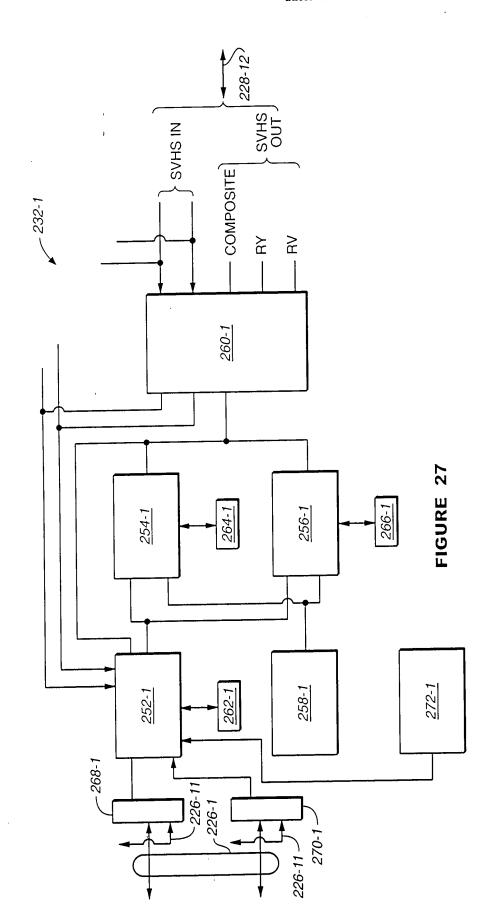


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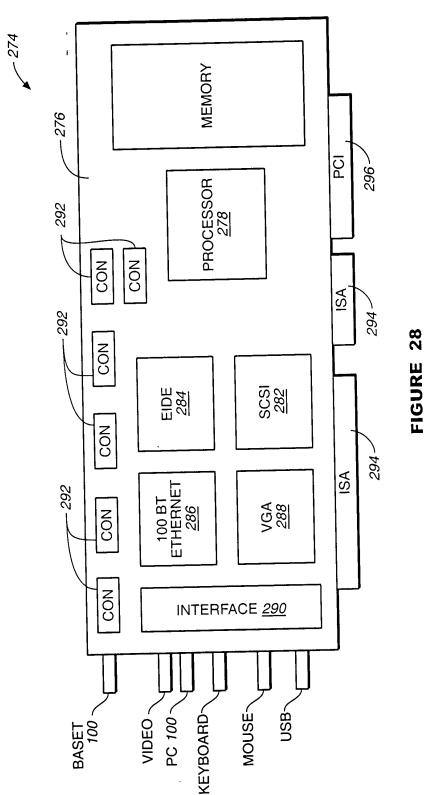


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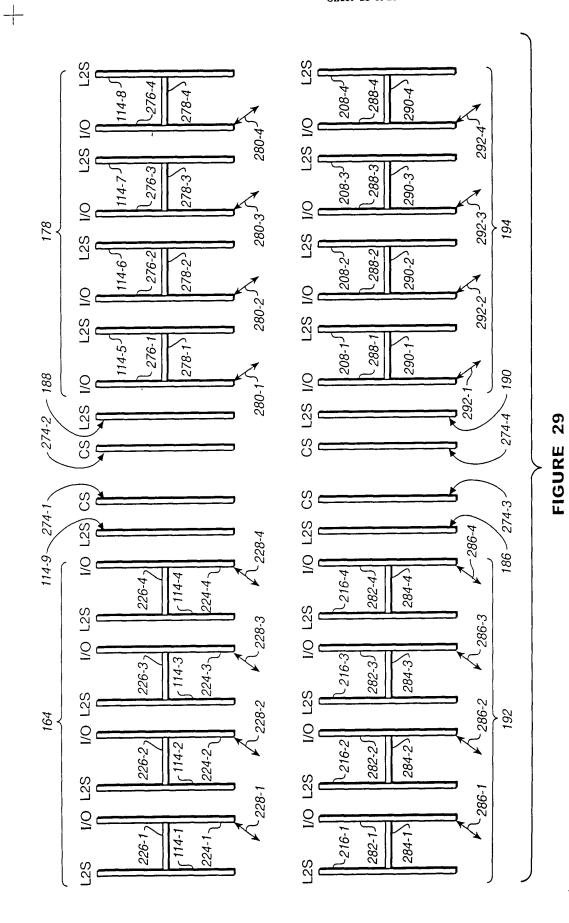
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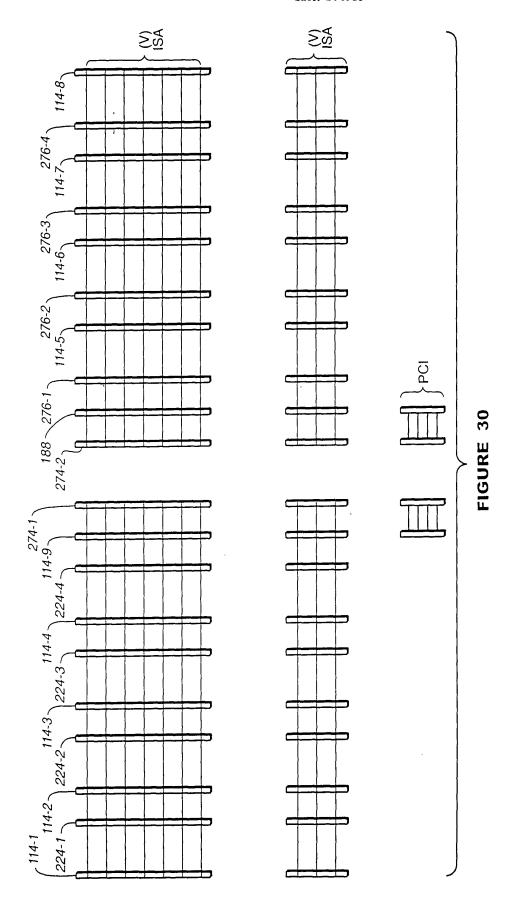
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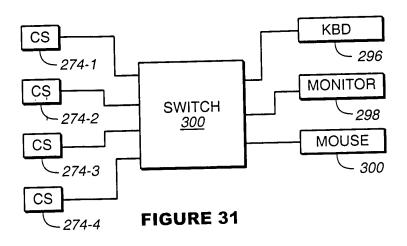


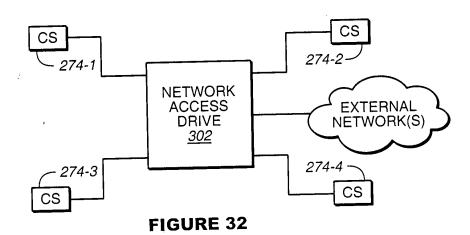
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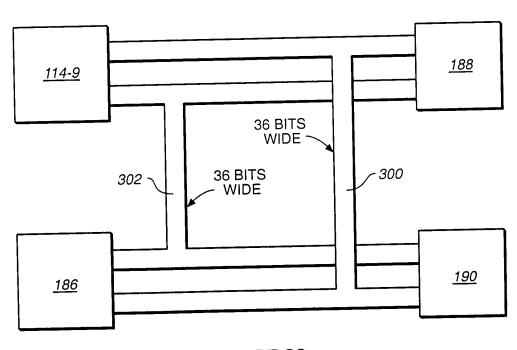


FIGURE 33